



iSPAN® 5576 PCI ATM OC-3/STM-1 Network Interface Card

Price/Performance-Maximized Wide Area ATM Connectivity

FEATURES

Fastest ATM OC-3/STM-1 card on the market! A new industry benchmark in throughput!

Software support for VxWorks®, Solaris™, and Linux®

Automatic Protection Switching (APS) for path guarantees

Mindspeed™ RS8234 155 Mbps ATM SAR

Mindspeed CX28250 PHY

Redundant physical interfaces for 155 Mbps connectivity

Support for ATM AAL5 and AAL0 Adaptation Layers

Constant Bit Rate (CBR), Variable Bit Rate (VBR) and Unspecified Bit Rate (UBR) services implemented in the hardware

Up to 64,000 simultaneous Virtual Connections (VCs) supported

OAM status notification Supported

iSAR API and command diagnostic utility

Future enhancements:

- SNMP Agent
- Classical IP (CLIP) Support

APPLICATIONS

Internet Access

Data Warehouse Systems

Remote Education and Training

DSLAMs

Gateways

Video Conferencing

Routers/Bridges

Integrated Access Devices (IADs)

VPNs

High Performance

Designed especially for high-availability, high-bandwidth access applications, the iSPAN 5576 provides ATM connectivity at 155 Mbps.

Reliable ATM Connectivity

Today's Networks require scalable, high-performance platforms with the flexibility to allow network capacity to keep pace with customer's demands. The iSPAN 5576 controller meets the needs of a variety of applications, including IP Switching and Routing, Internet connections and other applications that require the Quality of Service (QoS) guarantees provided by ATM.

Cost Effective

The low price and high performance qualities of the iSPAN 5576 make it the best 155Mbps value in the industry.





iSPAN 5576 OC-3/STM-1 Network Interface Card

Software Support

The 5576 is available with a Board Development Kit (BDK) for board & software development in Solaris™, VxWorks®, and Linux® environments. Included in this kit is the iSAR Application Programming Interface (API) to provide a seamless, consistent interface between the 5576 and supported operating systems. The Interphase-developed API and software layers enable application development without the need to be involved in low layer ATM driver development. The Automatic Protection Switching (APS) feature is hardware and software supported, enabling maximum uptime, reliability and network resiliency.

iSAR API

The iSAR API, included in the development kit, is an AAL layer API providing an efficient mechanism to enable configuration and control parameters for the driver, each adapter, and their respective Virtual Circuits (VCs). The API simplifies the development process by isolating the application from the complexities of the hardware while providing robust control of the hardware and data link.

Mindspeed RS8234 155 Mbps ATM SAR

Conforms to the ATM Forum recommendations

- 32-bit 33 MHz PCI 2.1 compliant with universal signaling
- UTOPIA 1 interface, with two PHY chip selects for APS capability
- 32-bit local bus interface
- Up to 32 K of total Virtual Channels bidirectional or 64 K unidirectional
- Hardware support of CBR, VBR and UBR services
- OAM functions support
- Up to 8 MB of control memory support

Mindspeed CX28250 PHY

- Meets ATM Forum standards
- 155 Mbps full duplex operations
- 16-bit UTOPIA2 interface (Interphase software kit allows user to select interface during 16-bit or 8-bit installation)
- 8-bit local bus interface (Interphase software kit allows user to select interface during 16-bit or 8-bit installation)
- Different levels of loopback control
- 512-word receive FIFO and 16-word transmit FIFO buffering

PCI Bus

PCI bus master features:

- 32-bit, zero wait transfers for up to 132 Mbps burst DMA rate
- 512-word receive FIFO and 16-word transmit FIFO buffering

PCI bus slave features:

- Address decoded with medium speed device
- Memory and configuration read & write cycles supported

Automatic Protection Switching

The RS8234 SAR has a UTOPIA 1 bus, with two PHY chip selects. In conjunction with the CX28250 PHYs, the second PHY chip select allows the second PHY to share the same UTOPIA bus for APS capability. Both PHYs transmit and receive all data. Both PHYs receive data from the redundant network, but only the primary PHY transfers data back to the ATM SAR. APS operating parameters and event notification are available through the API, including the selection of active and/or protective PHY.

Memory

- Up to 8 MB SRAM control memory for up to 64 K Virtual Connections
- (VCs)
- 1 KB of serial EEPROM

Clocks

The 5576 uses three clock sources: the PCI clock, the system clock and ATM front end clock.

Technical Specifications

Architecture

Bus Type	PCI 2.1 Compliant
Bus Data Transfer	32-bit, 33 MHz
Memory	4 or 8 MB control memory

Mechanical

Length	176 mm (6.93 in.) (without front panel)
Width	101.6 mm (4.0 in.)

Operating Environment

Temperature	0 to 55 °C (32 to 131 °F)
Relative Humidity	5% to 95% non-condensing
Altitude	0 to 15,000 ft.

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About Interphase Corporation

Interphase Corporation (NASDAQ: INPH) is a leading provider of robust building blocks, highly integrated subsystems and innovative gateway appliances for the converged communications network. Building on a 30-year history of providing advanced I/O solutions for telecom and enterprise applications, and addressing the need for high speed connectivity, Interphase has established a key leadership role in delivering next generation AdvancedTCA® and AdvancedMC™ solutions to the marketplace.